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TITLE OF THE INVENTION

METHOD AND APPARATUS FOR GENERATING
TRANSMISSION SIGNAL BY PROCESSING WHICH
INCLUDES DIGITAL QUADRATURE MODULATION

5 BACKGROUND OF THE INVENTIONField of the Invention

This invention relates to a method of generating a
transmission signal by processing which includes digital quadrature
modulation. Also, this invention relates to an apparatus for
10 generating a transmission signal by processing which includes
digital quadrature modulation.

Description of the Related Art

Japanese patent application publication number 8-102766
discloses digital quadrature modulators in which an I-channel (in-
15 phase channel) carrier is regarded as a repetitive data sequence of
"1"→"0"→"-1"→"0", and a Q-channel (quadrature channel) carrier is
regarded as a repetitive data sequence of "0"→"1"→"0"→"-1". A
digital I-channel information signal is sequentially multiplied by the
I-channel carrier data sequence, while a digital Q-channel
20 information signal is sequentially multiplied by the Q-channel
carrier data sequence. A signal generated by the multiplication in
the I-channel and a signal generated by the multiplication in the Q
channel are multiplexed into a digital-quadrature-modulation result
signal.

25 In the case where samples of the digital I-channel information
signal and samples of the digital Q-channel information signal are

synchronized with each other, the 90-degree ($\pi/2$) phase difference between the I-channel and Q-channel carrier data sequences causes a timing phase difference between the I-channel components and the Q-channel components of the digital-quadrature-modulation
5 result signal. Such a timing phase difference adversely affects signal transmission.

Japanese application 8-102766 discloses that digital filters are provided respectively in I-channel and Q-channel signal flow paths before a stage for multiplexing the digital I-channel and Q-
10 channel information signals by the I-channel and Q-channel carrier data sequences. The I-channel and Q-channel digital filters are designed to provide different signal phases to compensate for the timing phase difference between the I-channel components and the Q-channel components of the digital-quadrature-modulation result
15 signal.

The I-channel and Q-channel digital filters in Japanese application 8-102766 are required to implement accurate operation. In addition, the I-channel and Q-channel digital filters have complicated structures, and are hence expensive.

20 SUMMARY OF THE INVENTION

It is a first object of this invention to provide an improved method of generating a transmission signal by processing which includes digital quadrature modulation.

It is a second object of this invention to provide an improved
25 apparatus for generating a transmission signal by processing which includes digital quadrature modulation.

A first aspect of this invention provides a method of generating a transmission signal. The method comprises the steps of determining non-compensation signal points in a two-dimensional plane without considering a signal error caused by digital quadrature modulation, the two-dimensional plane being defined by a real axis and an imaginary axis, the real axis corresponding to real-part signal components, the imaginary axis corresponding to imaginary-part signal components; determining compensation signal points in the two-dimensional plane in response to a signal error caused by digital quadrature modulation if the non-compensation signal points are used, the non-compensation signal points and the compensation signal points being point-symmetry; sequentially assigning digital information signal pieces to one of the compensation signal points in response to contents of the digital information signal pieces; and subjecting the digital information pieces to a modulation process including digital quadrature modulation in response to the assignment given by the assigning step to generate a radio-frequency transmission signal.

A second aspect of this invention is based on the first aspect thereof, and provides a method wherein the compensation signal points provide compensation for an error in the radio-frequency transmission signal which is caused by one of a phase difference between an in-phase signal and a quadrature signal, an amplitude difference between the in-phase signal and the quadrature signal, and an error in a quadrature relation between the in-phase signal and the quadrature signal.

A third aspect of this invention is based on the first aspect thereof, and provides a method wherein the compensation signal points provide compensation for an error in the radio-frequency transmission signal which is caused by a timing difference between
5 an in-phase signal and a quadrature signal.

A fourth aspect of this invention provides a method of generating a transmission signal. The method comprises the steps of determining first non-compensation signal points in a two-dimensional plane without considering a signal error caused by
10 digital quadrature modulation, the two-dimensional plane being defined by a real axis and an imaginary axis, the real axis corresponding to real-part signal components, the imaginary axis corresponding to imaginary-part signal components; determining second non-compensation signal points in the two-dimensional
15 plane without considering the signal error caused by digital quadrature modulation; determining first compensation signal points in the two-dimensional plane for a first subcarrier in response to a signal error caused by digital quadrature modulation if the first non-compensation signal points are used, the first non-
20 compensation signal points and the first compensation signal points being point-symmetry; determining second compensation signal points in the two-dimensional plane for a second subcarrier in response to a signal error caused by digital quadrature modulation if the second non-compensation signal points are used, the second
25 non-compensation signal points and the second compensation signal points being point-symmetry, the second subcarrier being

equal in frequency to the first subcarrier and being different in polarity from the first subcarrier; sequentially assigning first digital information signal pieces to one of the first compensation signal points in response to contents of the first digital information signal pieces; sequentially assigning second digital information signal pieces to one of the second compensation signal points in response to contents of the second digital information signal pieces; and subjecting the first digital information pieces and the second digital information pieces to a modulation process including digital quadrature modulation in response to the assignments given by the assigning steps to generate a radio-frequency transmission signal containing the first and second subcarriers.

A fifth aspect of this invention provides an apparatus for generating a transmission signal. The apparatus comprises means for determining non-compensation signal points in a two-dimensional plane without considering a signal error caused by digital quadrature modulation, the two-dimensional plane being defined by a real axis and an imaginary axis, the real axis corresponding to real-part signal components, the imaginary axis corresponding to imaginary-part signal components; means for determining compensation signal points in the two-dimensional plane in response to a signal error caused by digital quadrature modulation if the non-compensation signal points are used, the non-compensation signal points and the compensation signal points being point-symmetry; means for sequentially assigning digital information signal pieces to one of the compensation signal points

in response to contents of the digital information signal pieces; and means for subjecting the digital information pieces to a modulation process including digital quadrature modulation in response to the assignment given by the assigning means to generate a radio-

5 frequency transmission signal.

A sixth aspect of this invention provides an apparatus for generating a transmission signal. The apparatus comprises first means for storing information representing assignment of states of a signal piece to signal points predetermined in response to an
10 estimated signal error caused by digital quadrature modulation in the absence of correction; second means for assigning an input information signal piece to one of the signal points in response to a state of the input information signal piece according to the information stored in the first means to convert the input
15 information signal piece into first and second baseband signal pieces; and third means for subjecting the first and second baseband signal pieces generated by the second means to a modulation process including digital quadrature modulation to convert the first and second baseband signal pieces into a piece of a
20 modulation-resultant transmission signal from which a signal error caused by the digital quadrature modulation is removed.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram of an OFDM (orthogonal frequency
division multiplexing) modulation apparatus according to an
25 embodiment of this invention.

Fig. 2 is a time-domain diagram of a first example of signals in

the apparatus of Fig. 1.

Fig. 3 is a time-domain diagram of a second example of signals in the apparatus of Fig. 1.

Fig. 4 is a diagram of a complex plane in which a state of a
5 subcarrier is illustrated.

Fig. 5 is a diagram of a complex plane in which vectors corresponding to a subcarrier are illustrated.

Fig. 6 is a diagram of a complex plane in which vectors corresponding to a subcarrier are illustrated.

10 Fig. 7 is a diagram of a complex plane in which vectors corresponding to a subcarrier are illustrated.

Fig. 8 is a diagram of a complex plane in which vectors corresponding to a subcarrier are illustrated.

15 Fig. 9 is a diagram of a complex plane in which vectors corresponding to a subcarrier are illustrated.

Fig. 10 is a diagram of a complex plane in which vectors corresponding to a subcarrier are illustrated.

DETAILED DESCRIPTION OF THE INVENTION

Fig. 1 shows an OFDM (orthogonal frequency division
20 multiplexing) modulation apparatus according to an embodiment of this invention.

The apparatus of Fig. 1 includes a data mapping circuit 11, a mapping table 12, an IFFT (inverse fast Fourier transform) device 13, a digital quadrature modulator 15, an intermediate-frequency
25 oscillator 16, and a D/A (digital-to-analog) converter 17. The data mapping circuit 11, the IFFT device 13, the digital quadrature

modulator 15, and the D/A converter 17 are sequentially connected in that order. The mapping table 12 is provided in a memory connected to the data mapping circuit 11. The intermediate-frequency oscillator 16 is connected to the digital quadrature
5 modulator 15.

The data mapping circuit 11 receives input digital data to be transmitted. The data mapping circuit 11 converts the input digital data into digital I (in-phase) signals "i" and digital Q (quadrature) signals "q" in response to the contents of the mapping table 12.
10 Specifically, the data mapping circuit 11 cyclically assigns successive segments of the input digital data to pairs of subcarriers of an OFDM signal, respectively. The mapping table 12 contains information indicating signal points of QPSK-resultant (quadrature phase shift keying resultant) subcarriers, QAM-resultant (quadrature
15 amplitude modulation resultant) subcarriers, or BPSK-resultant (binary phase shift keying resultant) subcarriers. The signal points are defined in an amplitude-angle plane. As will be made clear later, the signal points are predetermined so as to compensate for a signal error caused by the quadrature modulator 15. The mapping table
20 12 also contains information indicating each assignment of values which can be represented by a segment of the input digital data to signal points of a corresponding pair of QPSK-resultant subcarriers, QAM-resultant subcarriers, or BPSK-resultant subcarriers. The data mapping circuit 11 implements data mapping on the segments of
25 the input digital data in accordance with the contents of the mapping table as follows. Specifically, the data mapping circuit 11

assigns every segment of the input digital data to one of
corresponding signal points in response to the value represented by
the segment of the input digital data by referring to the contents of
the mapping table 12. In other words, for every segment of the
5 input digital data, the data mapping circuit 11 selects one from
among corresponding signal points in response to the value
represented by the segment of the input digital data. The data
mapping circuit 11 generates the digital signals "i" and the digital
signals "q" on the basis of the amplitude-direction coordinates and
10 the angle-direction coordinates of the assigned signal points (the
selected signal points). The data mapping circuit 11 feeds the
digital signals "i" and the digital signals "q" to the IFFT device 13.
The digital signals "i" and the digital signals "q" are arranged in a
virtual frequency domain so that they can be properly assigned to
15 frequencies (orthogonal baseband subcarrier frequencies) for IFFT.

The IFFT device 13 implements IFFT (inverse fast Fourier
transform) while setting the digital signals "i" as real-part terms and
setting the digital signals "q" as imaginary-part terms. The IFFT
device 13 converts and combines the digital signals "i" into an IFFT-
20 resultant digital I signal or an IFFT-resultant digital real-part signal.
In addition, the IFFT device 13 converts and combines the digital
signals "q" into an IFFT-resultant digital Q signal or an IFFT-
resultant digital imaginary-part signal. The IFFT device 13 outputs
the IFFT-resultant digital I and Q signals to the digital quadrature
25 modulator 15. The IFFT device 13 includes an n-point IFFT circuit,
where "n" denotes a predetermined natural number.

The digital quadrature modulator 15 includes multipliers 15A, 15B, 15C, and 15D, and a data selector 15E. The multipliers 15A, 15B, 15C, and 15D follow the IFFT device 13. The data selector 15E follows the multipliers 15A, 15B, 15C, and 15D. The data
5 selector 15E is connected to the intermediate-frequency oscillator 16 and the D/A converter 17.

The multiplier 15A receives the IFFT-resultant digital I signal from the IFFT device 13, and multiplies the IFFT-resultant digital I signal by "1". The multiplier 15A includes a buffer or an amplifier
10 having a gain of 1. The multiplier 15A outputs the multiplication-resultant digital signal "I" to the data selector 15E. The multiplier 15B receives the IFFT-resultant digital Q signal from the IFFT device 13, and multiplies the IFFT-resultant digital Q signal by "-1". The multiplier 15B includes an inverter. The multiplier 15B
15 outputs the multiplication-resultant digital signal "-Q" to the data selector 15E. The multiplier 15C receives the IFFT-resultant digital I signal from the IFFT device 13, and multiplies the IFFT-resultant digital I signal by "-1". The multiplier 15C includes an inverter. The multiplier 15C outputs the multiplication-resultant digital
20 signal "-I" to the data selector 15E. The multiplier 15D receives the IFFT-resultant digital Q signal from the IFFT device 13, and multiplies the IFFT-resultant digital Q signal by "1". The multiplier 15D includes a buffer or an amplifier having a gain of 1. The multiplier 15D outputs the multiplication-resultant digital signal "Q"
25 to the data selector 15E.

The intermediate-frequency oscillator 16 acts as a local

oscillator. The intermediate-frequency oscillator 16 includes a clock signal generator for producing a clock signal having a predetermined frequency. Preferably, the frequency of the clock signal is equal to a predetermined integer multiple of (for example, 5 four times) the frequency of virtual analog sine and cosine waves for quadrature modulation. The intermediate-frequency oscillator 16 feeds the clock signal to the data selector 15E.

10 The data selector 15E sequentially and cyclically selects one from among the output digital signals of the multipliers 15A, 15B, 15C, and 15D in response to the clock signal fed from the intermediate-frequency oscillator 16. The selection order in every cycle is as follows: "I" (the output digital signal of the multiplier 15A), "-Q" (the output digital signal of the multiplier 15B), "-I" (the output digital signal of the multiplier 15C), and "Q" (the output 15 digital signal of the multiplier 15D). The data selector 15E outputs the selection-resultant digital signal to the D/A converter 17.

The multipliers 15A-15D and the data selector 15E cooperate to implement digital quadrature modulation which combines the IFFT-resultant digital I and Q signals into a single quadrature- 20 modulation-resultant digital signal (the output digital signal from the data selector 15E). The digital quadrature modulation virtually uses a digital sine wave and a digital cosine wave having a frequency corresponding to the cycle of the signal selection by the data selector 15E. The digital sine wave periodically changes as 25 "0"→"1"→"0"→"-1". The digital cosine wave periodically changes as "1"→"0"→"-1"→"0". The digital sine wave results from periodically

sampling the virtual analog sine wave at a prescribed frequency.

The digital cosine wave results from periodically sampling the virtual analog cosine wave at the prescribed frequency. The multiplications by the multipliers 15A-15D correspond to the

5 multiplications among the IFFT-resultant digital I and Q signals, the digital sine wave, and the digital cosine wave for the digital quadrature modulation. For every cycle, the data selector 15E sequentially selects the output digital signals from the multipliers 15A-15D in the order of "I→-Q→-I→Q".

10 The D/A converter 17 changes the output digital signal of the digital quadrature modulator 15, that is, the output digital signal of the data selector 15E, into a corresponding analog multiple-carrier signal (a corresponding analog OFDM signal) whose center frequency is equal to the frequency of the sine and cosine waves
15 used in the digital quadrature modulator 15. The D/A converter 15 outputs the analog multiple-carrier signal.

The contents of the mapping table 12 include corrective data for previously correcting the error of the actual characteristics of the digital quadrature modulator 15 from the desired
20 characteristics. The data mapping by the data mapping circuit 11 in response to the corrective-data-added mapping table 12 means that non-corrected mapping-resultant signals are revised in response to the corrective data. The revision-resultant signals are used for generating the digital signals "i" and the digital signals "q".

25 The analog multiple-carrier signal (the analog OFDM signal) outputted from the D/A converter 15 is fed via a frequency up-

conversion stage to a receiving apparatus as a transmission signal while being propagated along a transmission line. The receiving apparatus demodulates the analog multiple-carrier signal into signal-point information pieces. The receiving apparatus recovers the
5 original digital data from the signal-point information pieces.

As shown in Fig. 2, the IFFT-resultant digital I signal outputted from the IFFT device 13 has a sequence of time-domain variable samples $\dots, I_{n-1}, I_n, I_{n+1}, \dots$. Similarly, the IFFT-resultant digital Q signal outputted from the IFFT device 13 has a sequence of
10 time-domain variable samples $\dots, Q_{n-1}, Q_n, Q_{n+1}, \dots$. The period of samples of the IFFT-resultant digital I signal and the period of samples of the IFFT-resultant digital Q signal correspond to a sampling frequency related to the OFDM signal. The sample period has a given relation with the window interval for operating
15 the n-point IFFT circuit in the IFFT device 13. Specifically, the sample period corresponds to the window interval divided by "n".

During every sample period, the data selector 15E sequentially and cyclically selects " I_n " (the output digital signal of the multiplier 15A), "0", " $-I_n$ " (the output digital signal of the
20 multiplier 15C), and "0". Thus, regarding the IFFT-resultant digital I signal, the data selector 15E generates the recurrence of " I_n ", "0", " $-I_n$ ", and "0". During every sample period, the data selector 15E sequentially and cyclically selects "0", " $-Q_n$ " (the output digital signal of the multiplier 15B)", "0", and " Q_n " (the output digital signal
25 of the multiplier 15D). Thus, regarding the IFFT-resultant digital Q signal, the data selector 15E generates the recurrence of "0", " $-Q_n$ ",

"0", and " Q_n ". The data selector 15E combines the recurrence of " I_n ", "0", " $-I_n$ ", and "0" and the recurrence of "0", " $-Q_n$ ", "0", and " Q_n " into the recurrence of " I_n ", " $-Q_n$ ", " $-I_n$ ", and " Q_n ". The data selector 15E outputs the recurrence of " I_n ", " $-Q_n$ ", " $-I_n$ ", and " Q_n "

5 as a modulation-resultant digital signal.

With reference to Fig. 2, at the start of every sample period, the real-part component " I_n " of the modulation-resultant digital signal (that is, the output signal from the data selector 15E) precedes the imaginary-part component " $-Q_n$ " thereof.

10 Accordingly, there would be a timing difference or a phase difference between the real-part components and the imaginary-part components of the modulation-resultant digital signal.

The data mapping circuit 11 and the mapping table 12 (see Fig. 1) are designed to compensate for such a timing difference (a phase difference). Specifically, the mapping table 12 contains data mapping information predetermined in response to an estimated timing difference between the real-part components and the imaginary-part components of the modulation-resultant digital signal generated by the digital quadrature modulator 15 in the absence of correction or compensation. The data mapping circuit 11 accesses the mapping table 12, and converts the input digital data into the digital signals "i" and the digital signals "q" by referring to the contents of the mapping table 12 (that is, the data mapping information in the mapping table 12). The digital signal "i" and the digital signal "q" provide compensation for the actual phase difference between the real-part components and the imaginary-

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part components of the modulation-resultant digital signal generated by the digital quadrature modulator 15. The data mapping circuit 11 feeds the digital signal "i" and the digital signal "q" to the IFFT device 13.

- 5 The timing difference or the phase difference between the real-part components and the imaginary-part components of the modulation-resultant digital signal generated by the digital quadrature modulator 15 corresponds to a timing error between the virtual sine wave and the virtual cosine wave used in the digital
- 10 quadrature modulator 15, that is, an error in the quadrature relation between I and Q signal vectors with respect to the modulation-resultant digital signal. The modulation-resultant digital signal has components (subcarriers) in upper and lower side bands of the intermediate frequency set by the intermediate-frequency oscillator
- 15 16. There are pairs of the upper-side-band signal components and the lower-side-band signal components. The upper-side-band signal component and the lower-side-band signal component in each pair (that is, the upper-side-band subcarrier and the lower-side-band subcarrier in each pair) are located at respective frequency points
- 20 equidistant from the intermediate frequency. The above-indicated error in the quadrature relation would cause crosstalk between the upper-side-band signal component and the lower-side-band signal component in each pair. The crosstalk would adversely alter the frequency spectrum of the modulation-resultant digital signal.
- 25 The data mapping circuit 11 and the mapping table 12 implement data mapping which is designed to compensate for such

crosstalk. Specifically, the data mapping is designed to correct the upper-side-band subcarrier and the lower-side-band subcarrier in each pair.

The data mapping will be described below in more detail.

- 5 Signal points are expressed by the coordinates in a complex plane. The data mapping uses signal points resulting from correction of original signal points. For example, an original signal point at the coordinates of (1, 1) is corrected into a second signal point at the coordinates of (1+x, 1+y) where "x" and "y" denote real-part and
10 imaginary-part corrective quantities respectively.

- The upper-side-band subcarrier and the lower-side-band subcarrier in each pair are defined as a positive-frequency subcarrier and a negative-frequency subcarrier, respectively. For the positive-frequency subcarrier and the negative-frequency
15 subcarrier in each pair, the corresponding before-correction signal point (the original signal point) is expressed by the values d1, d2, d3, and d4 which denote the real-part value for the positive-frequency subcarrier, the imaginary-part value for the positive-frequency subcarrier, the real-part value for the negative-frequency
20 subcarrier, and the imaginary-part value for the negative-frequency subcarrier, respectively. For example, in the case of QPSK, each of the values d1, d2, d3, and d4 assumes "+1" or "-1". The data mapping reflects correction of the values d1, d2, d3, and d4 into the values d1+x, d2+y, d3+x', and d4+y', respectively. Here, x, y, x',
25 and y' denote corrective quantities or corrective values.

Fig. 3 shows an example of time-domain states of the IFFT-

resultant digital I signal outputted from the IFFT device 13, the IFFT-resultant digital Q signal outputted from the IFFT device 13, the result of the I signal selection by the data selector 15E, the result of the Q signal selection by the data selector 15E, and the modulation-resultant digital signal outputted from the data selector 15E which occur under conditions as follows. The sample period of the IFFT-resultant digital I and Q signals is equal to 19.5 nsec.

Thus, the sample frequency of the IFFT-resultant digital I and Q signals is equal to 51.2 MHz. The period of the signal selection by the data selector 15E is equal to a half of the sample period of the IFFT-resultant digital I and Q signals. Thus, the sample frequency of the modulation-resultant digital signal is equal to 102.4 MHz. The intermediate frequency set by the intermediate-frequency oscillator 16 is equal to 25.6 MHz.

With reference to Fig. 3, the IFFT-resultant digital I signal has a sequence of time-domain variable samples $\dots, I_{n-1}, I_n, I_{n+1}, \dots$. Similarly, the IFFT-resultant digital Q has a sequence of time-domain variable samples $\dots, Q_{n-1}, Q_n, Q_{n+1}, \dots$. Each of the samples of the IFFT-resultant digital I and Q signals has a direct-current component (a zero-frequency component) and alternating-current components having frequencies up to 16 MHz. The digital quadrature modulator 15 handles the IFFT-resultant digital I and Q signals as baseband signals. The digital quadrature modulator 15 converts the IFFT-resultant digital I and Q signals into a modulation-resultant digital signal of an intermediate-frequency band centered at 25.6 MHz. Specifically, the modulation-resultant digital signal

extends over a frequency band of 25.6 ± 16 MHz. In the absence of the correction by the data mapping circuit 11 and the mapping table 12, the quadrature relation between I and Q signal vectors with respect to the modulation-resultant digital signal would have an error of about 9.8 nsec ($1/102.4$ MHz). A pair of the 38.4-MHz subcarrier and the 12.8-MHz subcarrier which is higher and lower than the center frequency (25.6 MHz) by 12.8 MHz is taken as an example. The quadrature-relation error would cause the 38.4-MHz subcarrier to have a phase delay of $\pi/4$ radian. On the other hand, the quadrature-relation error would cause the 12.8-MHz subcarrier to have a phase advance of $\pi/4$ radian. The previously-mentioned corrective quantities used in the data mapping compensate for such phase delays and phase advances in the subcarriers.

The corrective quantities (the corrective values) are expressed in a two-dimensional plane defined by the real axis and the imaginary axis. Fig. 4 illustrates a two-dimensional plane (a complex plane) defined by the real axis and the imaginary axis. In Fig. 4, there is shown a state of a subcarrier having a phase angle " α ", an angular velocity " $+\omega_n$ ", and an amplitude "A". The subcarrier is expressed by the following equation.

$$A \cdot \cos(+\omega_n t + \alpha) + j \cdot A \cdot \sin(+\omega_n t + \alpha) \quad \dots (1)$$

where "j" denotes an imaginary unit. For example, in the case of QPSK, the amplitude "A" is equal to $\sqrt{2}$, and the phase angle " α " assumes one of $\pi/4$, $3\pi/4$, $5\pi/4$, and $7\pi/4$.

Similarly, a subcarrier having a phase angle " β ", an angular velocity " $-\omega_n$ ", and an amplitude "B" is expressed by the following

equation.

$$B \cdot \cos(-\omega_n t + \beta) + j \cdot B \cdot \sin(-\omega_n t + \beta) \quad \dots (2)$$

For example, in the case of QPSK, the amplitude "B" is equal to $\sqrt{2}$, and the phase angle " β " assumes one of $\pi/4$, $3\pi/4$, $5\pi/4$, and $7\pi/4$.

- 5 The subcarrier having the angular velocity " $+\omega_n$ " and the subcarrier having the angular velocity " $-\omega_n$ " form a pair.

It is assumed that imaginary-part signals corresponding to the imaginary terms of the equations (1) and (2) have errors in amplitude and phase relative to real-part signals corresponding to
10 the real terms of the equations (1) and (2). Specifically, the assumed amplitude error is equal to " λ " times while the assumed phase error is equal to " γ " radian. The error-containing subcarriers are expressed by the following equations.

$$A \cdot \cos(+\omega_n t + \alpha) + j \cdot \lambda \cdot A \cdot \sin(+\omega_n t + \alpha - \gamma) \quad \dots (3)$$

15 $B \cdot \cos(-\omega_n t + \beta) + j \cdot \lambda \cdot B \cdot \sin(-\omega_n t + \beta + \gamma) \quad \dots (4)$

The phase error " γ " corresponds to the previously-mentioned error equal to about 9.8 nsec, that is, the quadrature-relation error. As will be explained later, the data mapping compensates for the phase error " γ ". In addition, the data mapping may compensate for the
20 amplitude error " λ ".

By using an exponential function, the equation (1) is rewritten as follows.

$$(a + j \cdot b) \cdot e^{j\omega_n t} \quad \dots (5)$$

where $a = A \cdot \cos(\alpha)$, and $b = A \cdot \sin(\alpha)$. The equation (3) is rewritten
25 as follows.

$$(1/2)Ae^{j\alpha}e^{j\omega_n t} + (1/2)Ae^{-j\alpha}e^{-j\omega_n t}$$

$$+ \lambda(1/2)Ae^{j(\alpha-\gamma)}e^{j\omega_n t} - \lambda(1/2)Ae^{-j(\alpha-\gamma)}e^{-j\omega_n t} \quad \dots (6)$$

Similarly, the equation (4) is rewritten as follows.

$$(1/2)Be^{j\beta}e^{-j\omega_n t} + (1/2)Be^{-j\beta}e^{j\omega_n t} \\ + \lambda(1/2)Be^{j(\beta+\gamma)}e^{-j\omega_n t} - \lambda(1/2)Be^{-j(\beta+\gamma)}e^{j\omega_n t} \quad \dots (7)$$

5 The first and third terms in the equation (6) denote vectors 61 and 63 rotating at the angular velocity " $\omega_n t$ " which are shown in Fig. 5. The first-term vector 61 in Fig. 5 has an amplitude " $A/2$ " and an angle " α " from the real axis. The third-term vector 63 in Fig. 5 has an amplitude " $\lambda \cdot A/2$ " and an angle " $\alpha - \gamma$ " from the real axis.

10 The second and fourth terms in the equation (6) denote vectors 62 and 64 rotating at the angular velocity " $-\omega_n t$ " which are shown in Fig. 6. The second-term vector 62 in Fig. 6 has an amplitude " $A/2$ " and an angle " $-\alpha$ " from the real axis. The fourth-term vector 64 in Fig. 6 has an amplitude " $\lambda \cdot A/2$ " and an angle " $-(\alpha - \gamma)$ " from the real axis. The fourth-term vector 64 exists in the second quadrant of the two-dimensional plane.

15 The first-term vector 61 in Fig. 5 and the second-term vector 62 in Fig. 6 correspond to the real-part signal expressed by the first term of the equation (1), while the third-term vector 63 in Fig. 5 and the fourth-term vector 64 in Fig. 6 correspond to the imaginary-part signal expressed by the second term of the equation (1).

20 The first and third terms in the equation (7) denote vectors 71 and 73 rotating at the angular velocity " $-\omega_n t$ " which are shown in Fig. 7. The first-term vector 71 in Fig. 7 has an amplitude " $B/2$ " and an angle " β " from the real axis. The third-term vector 73 in Fig. 7

has an amplitude " $\lambda \cdot B/2$ " and an angle " $\beta + \gamma$ " from the real axis.

The second and fourth terms in the equation (7) denote vectors 72 and 74 rotating at the angular velocity " $\omega_n t$ " which are shown in Fig. 8. The second-term vector 72 in Fig. 8 has an
5 amplitude " $B/2$ " and an angle " $-\beta$ " from the real axis. The fourth-term vector 74 in Fig. 8 has an amplitude " $\lambda \cdot B/2$ " and an angle " $-(\beta + \gamma)$ " from the real axis. The fourth-term vector 74 exists in the second quadrant of the two-dimensional plane.

The first-term vector 71 in Fig. 7 and the second-term vector
10 72 in Fig. 8 correspond to the real-part signal expressed by the first term of the equation (2), while the third-term vector 73 in Fig. 7 and the fourth-term vector 74 in Fig. 8 correspond to the imaginary-part signal expressed by the second term of the equation (2).

15 The previously-mentioned amplitude error (" λ " differs from "1") and phase error (" γ " differs from "0") relate to the third and fourth terms in the equations (6) and (7). The data mapping is designed to compensate for the amplitude error " λ " and the phase error " γ ". Specifically, in the case where a corrective signal having
20 an amplitude of a factor of " $1/\lambda$ " and a phase advance of " γ " is given with respect to the third and fourth terms in the equation (6), the digital quadrature modulator 15 multiplies the amplitude by " λ " and delays the phase by " γ " so that a signal having a nullified amplitude error and a nullified phase error will be generated regarding the
25 third and fourth terms of the equation (6).

The corrective signal for the signal represented by the

equation (6) is expressed as follows.

$$(1/2)Ae^{j\alpha}e^{j\omega_n t} + (1/2)Ae^{-j\alpha}e^{-j\omega_n t} \\ + \{1/(2\lambda)\}Ae^{j(\alpha+\gamma)}e^{j\omega_n t} - \{1/(2\lambda)\}Ae^{-j(\alpha+\gamma)}e^{-j\omega_n t} \dots (8)$$

Similarly, a corrective signal for the signal represented by the
5 equation (7) is expressed as follows.

$$(1/2)Be^{j\beta}e^{-j\omega_n t} + (1/2)Be^{-j\beta}e^{j\omega_n t} \\ + \{1/(2\lambda)\}Be^{j(\beta-\gamma)}e^{-j\omega_n t} - \{1/(2\lambda)\}Be^{-j(\beta-\gamma)}e^{j\omega_n t} \dots (9)$$

The corrective signals expressed by the equations (8) and (9)
correspond to the cancel of the amplitude error " λ " and the phase
10 error " γ " from the before-correction signals given by the equations
(3) and (4). Accordingly, it is possible to generate error-free
modulation-resultant digital signals expressed by the equations (1)
and (2).

Fig. 9 shows a mapping point (a signal point) for enabling the
15 digital quadrature modulator 15 to generate an error-compensated
modulation-resultant digital signal having the angular velocity " $\omega_n t$ ".
In Fig. 9, vectors 81 and 83 correspond to the first and third terms
of the equation (8) while vectors 92 and 94 correspond to the
second and fourth terms of the equation (9). A final vector 101
20 representing a correction-resultant signal point is calculated by
summing the vectors 81, 83, 92, and 94. The vectors 81 and 83
correspond to the terms containing " $e^{j\omega_n t}$ ". Also, the vectors 92
and 94 correspond to the terms containing " $e^{j\omega_n t}$ ". Accordingly,
the final vector 101 is equal to the resultant of the vectors 81, 83,
25 92, and 94 which rotate at the angular velocity " $\omega_n t$ ". Specifically,
the final vector 101 is calculated by summing the resultant of the

vectors 81 and 92 and the resultant of the vectors 83 and 94. The correction-resultant signal point represented by the final vector 101 is assigned to the subcarrier which rotates at the angular velocity " $\omega_n t$ ".

5 Fig. 10 shows a mapping point (a signal point) for enabling the digital quadrature modulator 15 to generate an error-compensated modulation-resultant digital signal having the angular velocity " $-\omega_n t$ ". In Fig. 10, vectors 82 and 84 correspond to the second and fourth terms of the equation (8) while vectors 91 and 93
10 correspond to the first and third terms of the equation (9). A final vector 102 representing a correction-resultant signal point is calculated by summing the vectors 82, 84, 91, and 93. The vectors 82 and 84 correspond to the terms containing " $e^{-j\omega_n t}$ ". Also, the vectors 91 and 93 correspond to the terms containing " $e^{-j\omega_n t}$ ".
15 Accordingly, the final vector 102 is equal to the resultant of the vectors 82, 84, 91, and 93 which rotate at the angular velocity " $-\omega_n t$ ". The correction-resultant signal point represented by the final vector 102 is assigned to the subcarrier which rotates at the angular velocity " $-\omega_n t$ ".

20 As understood from the previous description, in order to compensate for the phase error and the amplitude error caused by the digital quadrature modulator 15 to generate the error-free subcarriers " $A \cdot \cos(+\omega_n t + \alpha) + j \cdot A \cdot \sin(+\omega_n t + \alpha)$ " and " $B \cdot \cos(-\omega_n t + \beta) + j \cdot B \cdot \sin(-\omega_n t + \beta)$ ", the signal points used by the data mapping are
25 chosen to correspond to subcarriers " $A \cdot \cos(+\omega_n t + \alpha) + j \cdot (1/\lambda) \cdot A \cdot \sin(+\omega_n t + \alpha + \gamma)$ " and " $B \cdot \cos(-\omega_n t + \beta) +$

$$j \cdot (1/\lambda) \cdot B \cdot \sin(-\omega_n t + \beta - \gamma)''.$$

The sum of the coefficients in the terms of the equations (8) and (9) which relate to the subcarrier having the angular frequency "+ ω_n " is given as follows.

$$5 \quad (1/2)Ae^{j\alpha} + (1/2)Be^{-j\beta} + \{1/(2\lambda)\}Ae^{j(\alpha+\gamma)} - \{1/(2\lambda)\}Be^{-j(\beta-\gamma)} \quad \dots (10)$$

The equation (10) is rewritten as follows.

$$\text{Re}(+\omega_n) + j\text{Im}(+\omega_n) \quad \dots (11)$$

where:

$$10 \quad \begin{aligned} \text{Re}(+\omega_n) &= (1/2)A\cos\alpha + (1/2)B\cos\beta \\ &\quad + \{1/(2\lambda)\}A\cos(\alpha+\gamma) - \{1/(2\lambda)\}B\cos(\beta-\gamma) \quad \dots (12) \\ \text{Im}(+\omega_n) &= (1/2)A\sin\alpha - (1/2)B\sin\beta \\ &\quad + \{1/(2\lambda)\}A\sin(\alpha+\gamma) + \{1/(2\lambda)\}B\sin(\beta-\gamma) \quad \dots (13) \end{aligned}$$

The value $\text{Re}(+\omega_n)$ denoted by the equation (12) is assigned to the real part for the subcarrier having the angular frequency "+ ω_n ". The value $\text{Im}(+\omega_n)$ denoted by the equation (13) is assigned to the imaginary part for the subcarrier having the angular frequency "+ ω_n ".

The sum of the coefficients in the terms of the equations (8) and (9) which relate to the subcarrier having the angular frequency "- ω_n " is given as follows.

$$20 \quad (1/2)Be^{j\beta} + (1/2)Ae^{-j\alpha} + \{1/(2\lambda)\}Be^{j(\beta-\gamma)} - \{1/(2\lambda)\}Ae^{-j(\alpha+\gamma)} \quad \dots (14)$$

The equation (13) is rewritten as follows.

$$25 \quad \text{Re}(-\omega_n) + j\text{Im}(-\omega_n) \quad \dots (15)$$

where:

$$\begin{aligned} \text{Re}(-\omega_n) &= (1/2)B\cos\beta + (1/2)A\cos\alpha \\ &\quad + \{1/(2\lambda)\}B\cos(\beta-\gamma) - \{1/(2\lambda)\}A\cos(\alpha+\gamma) \quad \dots (16) \\ \text{Im}(-\omega_n) &= (1/2)B\sin\beta - (1/2)A\sin\alpha \\ &\quad + \{1/(2\lambda)\}B\sin(\beta-\gamma) + \{1/(2\lambda)\}A\sin(\alpha+\gamma) \quad \dots (17) \end{aligned}$$

5 The value $\text{Re}(-\omega_n)$ denoted by the equation (16) is assigned to the real part for the subcarrier having the angular frequency " $-\omega_n$ ". The value $\text{Im}(-\omega_n)$ denoted by the equation (17) is assigned to the imaginary part for the subcarrier having the angular frequency " $-\omega_n$ ".

10 Since the error caused by the digital quadrature modulator 15 relates to the calculation time for the imaginary-part signals, the phase error is present while the amplitude error is basically absent. Therefore, the value " λ " is set to "1" while the value " γ " differs from "0". In the case of QPSK, the amplitudes "A" and "B" are equal to each other. Thus, the equations (12), (13), (16), and (17) are
15 rewritten as follows.

$$\text{Re}(+\omega_n) = (1/2)A\{\cos\alpha + \cos\beta + \cos(\alpha+\gamma) - \cos(\beta-\gamma) \quad \dots (18)$$

$$\text{Im}(+\omega_n) = (1/2)A\{\sin\alpha - \sin\beta + \sin(\alpha+\gamma) + \sin(\beta-\gamma) \quad \dots (19)$$

$$\text{Re}(-\omega_n) = (1/2)A\{\cos\beta + \cos\alpha + \cos(\beta-\gamma) - \cos(\alpha+\gamma) \quad \dots (20)$$

20 $\text{Im}(-\omega_n) = (1/2)B\{\sin\beta - \sin\alpha + \sin(\beta-\gamma) + \sin(\alpha+\gamma) \quad \dots (21)$

In the case of QPSK, each of the angles " α " and " β " assumes one of $\pi/4$, $3\pi/4$, $5\pi/4$, and $7\pi/4$. The values $\text{Re}(+\omega_n)$, $\text{Im}(+\omega_n)$, $\text{Re}(-\omega_n)$, and $\text{Im}(-\omega_n)$ expressed by the equations (18)-(21) depend on the angles " α " and " β " as follows.

25 When the angles " α " and " β " are equal to $\pi/4$, the values $\text{Re}(+\omega_n)$, $\text{Im}(+\omega_n)$, $\text{Re}(-\omega_n)$, and $\text{Im}(-\omega_n)$ are equal to " $1 - \sin\gamma$ ",

" $\cos\gamma$ ", " $1 + \sin\gamma$ ", and " $\cos\gamma$ " respectively.

When the angles " α " and " β " are equal to $\pi/4$ and $3\pi/4$ respectively, the values $\text{Re}(+\omega_N)$, $\text{Im}(+\omega_N)$, $\text{Re}(-\omega_N)$, and $\text{Im}(-\omega_N)$ are equal to " $\sqrt{2}\cos(\pi/4+\gamma)$ ", " $\sqrt{2}\sin(\pi/4+\gamma)$ ", " $\sqrt{2}\cos(3\pi/4-\gamma)$ ", and
5 " $\sqrt{2}\sin(3\pi/4-\gamma)$ " respectively.

When the angles " α " and " β " are equal to $\pi/4$ and $5\pi/4$ respectively, the values $\text{Re}(+\omega_N)$, $\text{Im}(+\omega_N)$, $\text{Re}(-\omega_N)$, and $\text{Im}(-\omega_N)$ are equal to " $\cos\gamma$ ", " $1 + \sin\gamma$ ", " $-\cos\gamma$ ", and " $-1 + \sin\gamma$ " respectively.

When the angles " α " and " β " are equal to $\pi/4$ and $7\pi/4$
10 respectively, the values $\text{Re}(+\omega_N)$, $\text{Im}(+\omega_N)$, $\text{Re}(-\omega_N)$, and $\text{Im}(-\omega_N)$ are equal to "1", "1", "1", and "-1" respectively.

When the angles " α " and " β " are equal to $3\pi/4$ and $\pi/4$ respectively, the values $\text{Re}(+\omega_N)$, $\text{Im}(+\omega_N)$, $\text{Re}(-\omega_N)$, and $\text{Im}(-\omega_N)$ are equal to " $\sqrt{2}\cos(3\pi/4+\gamma)$ ", " $\sqrt{2}\sin(3\pi/4+\gamma)$ ", " $\sqrt{2}\cos(\pi/4-\gamma)$ ", and
15 " $\sqrt{2}\sin(\pi/4-\gamma)$ " respectively.

When the angles " α " and " β " are equal to $3\pi/4$, the values $\text{Re}(+\omega_N)$, $\text{Im}(+\omega_N)$, $\text{Re}(-\omega_N)$, and $\text{Im}(-\omega_N)$ are equal to " $-1 - \sin\gamma$ ", " $\cos\gamma$ ", " $-1 + \sin\gamma$ ", and " $\cos\gamma$ " respectively.

When the angles " α " and " β " are equal to $3\pi/4$ and $5\pi/4$
20 respectively, the values $\text{Re}(+\omega_N)$, $\text{Im}(+\omega_N)$, $\text{Re}(-\omega_N)$, and $\text{Im}(-\omega_N)$ are equal to "-1", "1", "-1", and "-1" respectively.

When the angles " α " and " β " are equal to $3\pi/4$ and $7\pi/4$ respectively, the values $\text{Re}(+\omega_N)$, $\text{Im}(+\omega_N)$, $\text{Re}(-\omega_N)$, and $\text{Im}(-\omega_N)$ are equal to " $-\cos\gamma$ ", " $1 - \sin\gamma$ ", " $\cos\gamma$ ", and " $-1 - \sin\gamma$ " respectively.

25 When the angles " α " and " β " are equal to $5\pi/4$ and $\pi/4$ respectively, the values $\text{Re}(+\omega_N)$, $\text{Im}(+\omega_N)$, $\text{Re}(-\omega_N)$, and $\text{Im}(-\omega_N)$

are equal to $-\cos\gamma$, $-1 - \sin\gamma$, $\cos\gamma$, and $1 - \sin\gamma$ respectively.

When the angles α and β are equal to $5\pi/4$ and $3\pi/4$ respectively, the values $\text{Re}(+\omega_n)$, $\text{Im}(+\omega_n)$, $\text{Re}(-\omega_n)$, and $\text{Im}(-\omega_n)$ are equal to -1 , -1 , -1 , and 1 respectively.

- 5 When the angles α and β are equal to $5\pi/4$, the values $\text{Re}(+\omega_n)$, $\text{Im}(+\omega_n)$, $\text{Re}(-\omega_n)$, and $\text{Im}(-\omega_n)$ are equal to $-1 + \sin\gamma$, $-\cos\gamma$, $-1 - \sin\gamma$, and $-\cos\gamma$ respectively.

- 10 When the angles α and β are equal to $5\pi/4$ and $7\pi/4$ respectively, the values $\text{Re}(+\omega_n)$, $\text{Im}(+\omega_n)$, $\text{Re}(-\omega_n)$, and $\text{Im}(-\omega_n)$ are equal to $\sqrt{2}\cos(5\pi/4+\gamma)$, $\sqrt{2}\sin(5\pi/4+\gamma)$, $\sqrt{2}\cos(7\pi/4-\gamma)$, and $\sqrt{2}\sin(7\pi/4-\gamma)$ respectively.

When the angles α and β are equal to $7\pi/4$ and $\pi/4$ respectively, the values $\text{Re}(+\omega_n)$, $\text{Im}(+\omega_n)$, $\text{Re}(-\omega_n)$, and $\text{Im}(-\omega_n)$ are equal to 1 , -1 , 1 , and 1 respectively.

- 15 When the angles α and β are equal to $7\pi/4$ and $3\pi/4$ respectively, the values $\text{Re}(+\omega_n)$, $\text{Im}(+\omega_n)$, $\text{Re}(-\omega_n)$, and $\text{Im}(-\omega_n)$ are equal to $\cos\gamma$, $-1 + \sin\gamma$, $-\cos\gamma$, and $1 + \sin\gamma$ respectively.

- 20 When the angles α and β are equal to $7\pi/4$ and $5\pi/4$ respectively, the values $\text{Re}(+\omega_n)$, $\text{Im}(+\omega_n)$, $\text{Re}(-\omega_n)$, and $\text{Im}(-\omega_n)$ are equal to $\sqrt{2}\cos(7\pi/4+\gamma)$, $\sqrt{2}\sin(7\pi/4+\gamma)$, $\sqrt{2}\cos(5\pi/4-\gamma)$, and $\sqrt{2}\sin(5\pi/4-\gamma)$ respectively.

When the angles α and β are equal to $7\pi/4$, the values $\text{Re}(+\omega_n)$, $\text{Im}(+\omega_n)$, $\text{Re}(-\omega_n)$, and $\text{Im}(-\omega_n)$ are equal to $1 + \sin\gamma$, $-\cos\gamma$, $1 - \sin\gamma$, and $-\cos\gamma$ respectively.

- 25 It should be noted that QPSK may be replaced by BPSK (binary phase-shift keying) or multi-value QAM. The previously-mentioned

compensation for the modulation error caused by the timing phase difference can be applied to any modulation system in which a positive subcarrier frequency and a negative subcarrier frequency are set relative to a central frequency, and information pieces to be
5 transmitted are assigned to signal points of the positive and negative subcarriers.

In the case where both the phase error and the amplitude error are present so that the value " λ " differs from "1" and the value " γ " differs from "0", the correction-resultant signal points used by
10 the data mapping are determined on the basis of the equations (12), (13), (16), and (17). In this case, it is possible to correct both the phase error and the amplitude error.

It should be noted that the data selector 15E in the digital quadrature modulator 15 may generate the recurrence of " Q_n ", " I_n ",
15 " $-Q_n$ ", and " $-I_n$ " or the recurrence of " $-Q_n$ ", " $-I_n$ ", " Q_n ", and " I_n " instead of the recurrence of " I_n ", " $-Q_n$ ", " $-I_n$ ", and " Q_n ".